



SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION

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Marc Tremblay, et al.

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U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
RF	WO 01 93028 A2	6 December 2001	PCT				
RF	WO 03/054693	3 July 2003	PCT				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

RF	"The Potential for Using Thread-Level Data Speculation to Facilitate Automatic Parallelization", by J. Gregory Steffan et al., 1998, IEEE, Pages 2-13.

EXAMINER

/Robert Fennema/

DATE CONSIDERED

10/30/2006

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